

GENERAL PURPOSE
HIGH ISOLATION VOLTAGE
SINGLE TRANSISTOR TYPE
HIGH COLLECTOR VOLTAGE
PHOTOCOUPLER SERIES

FEATURES

- 1. High isolation voltage between input and output (Viso=5000 Vrms).
- 2. High Collector-emitter voltage (Vceo=70V).
- 3.Compact dual-in-line package KB836:3-channel type
- 4. Recognized by UL and CUL, file NO. E225308.
- 5.RoHS compliant.

DESCRIPTION

- 1. The KB836 (3-channel) is optically coupled isolators containing
 - a GaAS light emitting diode and an NPN silicon phototransistor.
- 2.The lead pitch is 2.54mm.
- 3.Solid insulation thickness between emitting diode and output phototransistor:>=0.6mm.

APPLICATIONS

- 1.Computer terminals.
- 2. Registers, copiers, automatic vending machines.
- 3. System appliances, measuring instruments.
- 4. Programmable logic controller.
- 5. Signal transmission between circuits of different potentials and impedances.

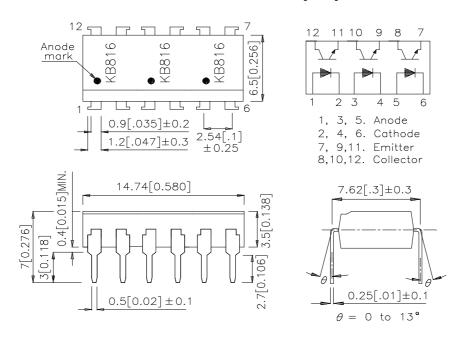
 SPEC NO: DSAD1547
 REV NO: V.4
 DATE: MAR/17/2005
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 APPROVED: J. Lu
 CHECKED: Tracy Deng
 DRAWN: Y.CHENG
 ERP: 1205000040



*PACKAGE DIMENSIONS (UNIT:mm)

TOLERANCE: ±0.5[±0.02] UNLESS OTHERWISE NOTED.



*Absolute Maximum Ratings (Ta=25°C)

	Parameter	Symbol	Rating	Unit
	Forward current	I _F	50	mA
Input	Reverse voltage	V _R	6	V
	Power dissipation	P	70	mW
Output	Collector-emitter voltage	V _{CEO}	70	V
	Emitter-collector voltage	V _{ECO}	6	V
	Collector current	I _c	50	mA
	Collector power dissipation	P _c	150	mW
Total pov	ver dissipation	Ptot	200	mW
¹¹ Isolation voltage		Viso	5000	Vrms
Operating temperature		Topr	-30~+100	°C
Storage temperature		Tstg	-55~+125	°C
² Soldering temperature		Tsol	260	°C

^{*1 40} to 60% RH,AC for 1 minute.

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^{*2} For 10 seconds.



*Electro-optical Characteristics (Ta=25°C)

	Parameter		Symbol	Conditions	Min.	Тур.	Max.	Unit
Input	Forward voltage		VF	I=20mA		1.2	1.4	V
	Peak forward voltage		VFM	Iғм=0.5A			3.0	V
	Reverse current		l R	V _R =4V			10	μΑ
Output	Collector dark cur	rent	ICEO	Vce=20V,IF=0mA			10 ⁻⁷	Α
Transfer charact- eristics	*1Current transfer i	ratio	CTR	IF=5mA,VcE=5V	50		600	%
	Collector-emitter s	saturation voltage	VCE(sat)	IF=20mA, Ic=1mA		0.1	0.2	V
	Cut-off frequency		fc	VcE=5V, lc=2mA RL=100Ω,-3dB		80		KHz
	Response time	Rise time	tr	VCE=2V, lc=2mA RL=100 Ω		4	18	μs
		Fall time	tf			3	18	μs

*1 Classification table of current transfer ratio is shown below.

$$CTR = \frac{Ic}{I_F} \times 100\%$$

Model NO.	Rank mark	CTR(%)
KB836L	L	50 to 100
KB836A	A	80 to 160
KB836B	В	130 to 260
KB836C	С	200 to 400
KB836D	D	300 to 600
KB836AB	A or B	80 to 260
KB836BC	B or C	130 to 400
KB836CD	C or D	200 to 600
KB836AC	A,B or C	80 to 400
KB836BD	B,C or D	130 to 600
KB836AD	A,B,C or D	80 to 600
KB836	L,A,B,C,D or No mark	50 to 600

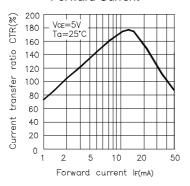
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Fig. 1 Current Transfer Ratio vs. Forward Current



Forward voltage

Fig. 2 Forward Current vs.

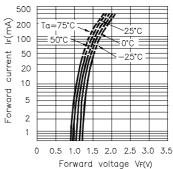


Fig. 3 Collector Current vs.
Collector-emitter Voltage

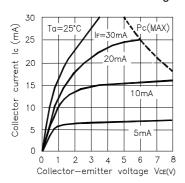


Fig. 4 Relative Current Transfer Ratio vs. Ambient Temperature

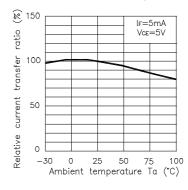


Fig. 5 Collector-emitter Saturation Voltage vs. Ambient Temperature

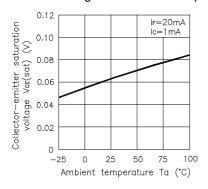
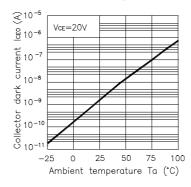


Fig. 6 Collector Dark Current vs.
Ambient Temperature



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Fig. 7 Forward Current vs.

Ambient Temperature

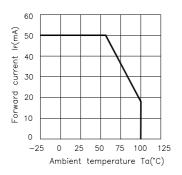


Fig. 8 Collector Power Dissipation vs.
Ambient Temperature

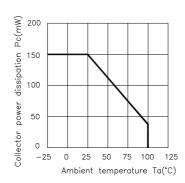
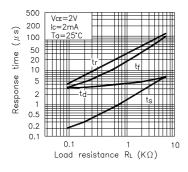


Fig. 9 Response Time vs. Load Resistance



Test Circuit for Response Time

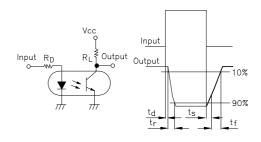
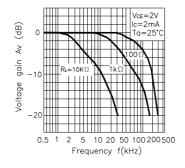
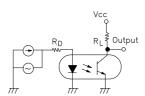


Fig. 10 Frequency Response



Test Circuit for Frequency Response

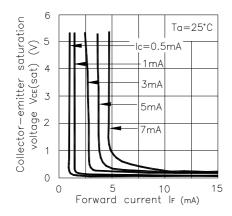


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Fig. 11 Collector-emitter Saturation Voltage vs. Forward Current



*NOTES ON HANDLING

1.Recommended soldering conditions (Dip soldering)

(1) Dip soldering

Temperature 260 °C or below (molten solder temperature)

Time Less than 10 seconds.

Cycle One cycle allowed to be dipped in solder including plastic mold portion.

Flux Rosin flux containing small amount of chorine

(The flux with a maximum chlorine content of 0.2 Wt % is recommended.)

(2) Cautions

Fluxes

Avovid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that power is suddenly into the componment any surge current may cause damage happen, even if the voltage is within the absolute maximum ratings.

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CAUTION

Within this device there exists GaAs (Gallium Arsenide) material which is a harmful substance if ingested.

GaAs dust and fumes are toxic. Do not break, cut or pulverize the product, or use chemicals to dissolve them.

RESTRICTIONS ON PRODUCT USE

- The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices / types available in every country.
- We are mention about our product quality stablity, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing KINGBRIGHT products, to observe standards of safety, and to a avoid situations in which a malfunction or failure of a KINGBRIGHT product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that KINGBRIGHT products are used within specified operating ranges as set forth in the most recent products specifications.

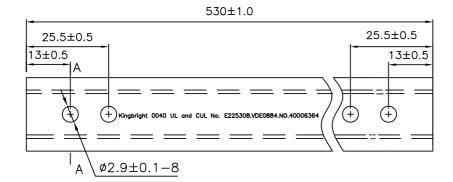
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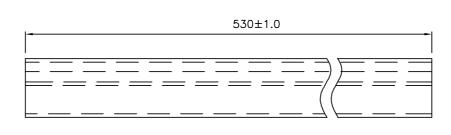


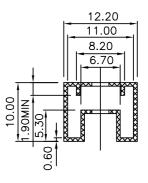
Dimension of Tube

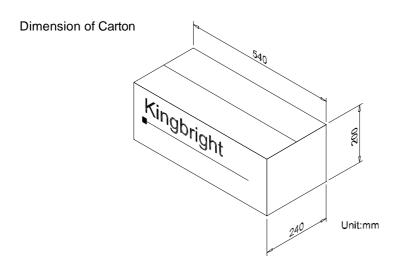
 $\label{eq:tolerance: $\pm 0.4[\pm 0.012]$ UNLESS OTHERWISE NOTED.}$ Unit:mm



A-A Side view







Part Number	Package	Package Style
KB836	12-pin DIP	30pcs/each tube